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1	US 20050180410 A1	20050818	29	Distributed switch architecture including a growth input/output bus structure	370/386
2	US 20050088314 A1	20050428	489	Radio frequency data communications device	340/825.36
3	US 20050017774 A1	20050127	15	Precision closed loop delay line for wide frequency data recovery	327/156
4	US 20040247040 A1	20041209	14	Electromagnetic wave transmitter systems, methods and articles of manufacture	375/295
5	US 20040232951 A1	20041125	9	Lock detector for phase locked loops	327/105
6	US 20040201457 A1	20041014	490	Radio frequency data communications device	340/10.33
7	US 20030212930 A1	20031113	36	Clock data recovery circuitry associated with programmable logic device circuitry	714/700
8	US 20030197565 A1	20031023	9	Lock detector for phase locked loops	331/17
9	US 20030099210 A1	20030529	503	RADIO FREQUENCY DATA COMMUNICATIONS DEVICE	370/311
10	US 20030043949 A1	20030306	3353	Radio frequency data communications device	375/374
11	US 20020184577 A1	20021205	15	Precision closed loop delay line for wide frequency data recovery	714/700
12	US 20020174153 A1	20021121	3354	Radio frequency data communications device	708/252
13	US 20020159130 A1	20021031	16	Transponder and wavelength division-multiplexing optical transmission equipment	359/326

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1		Pepenella, Gerard
2		O'Toole, James E. et al.
3		Chow, James et al.
4		Dennis, Anthony et al.
5		Tan, Loke Kun et al.
6		O'Toole, James E. et al.
7		Aung, Edward et al.
8		Tan, Loke Kun et al.
9		O'TOOLE, JAMES E. et al.
10		O'Toole, James E. et al.
11		Chow, James et al.
12	340/10.1; 370/311	O'Toole, James E. et al.
13		Sakano, Shinji et al.

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14	US 20010050580 A1	20011213	501	Radio frequency data communications device	327/158
15	US 20010033188 A1	20011025	37	Clock data recovery circuitry associated with programmable logic device circuitry	327/141
16	US 20010026240 A1	20011004	17	Personal location detection system	342/357.07
17	US 20010024142 A1	20010927	9	Lock detector for phase locked loops	331/25
18	US 6985016 B2	20060110	15	Precision closed loop delay line for wide frequency data recovery	327/149
19	US 6947513 B2	20050920	54	Radio frequency data communications device	375/374
20	US 6879627 B1	20050412	30	Variable rate continuous mode satellite modem	375/222
21	US 6836472 B2	20041228	52	Radio frequency data communications device	370/335
22	US 6836468 B1	20041228	56	Radio frequency data communications device	370/310
23	US 6825773 B1	20041130	54	Radio frequency data communications device	340/825.72

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14		O'Toole, James E. et al.
15	326/41; 375/324	Aung, Edward et al.
16		Neher, Timothy J.
17	331/DIG. 2	Tan, Loke Kun et al.
18	327/153; 327/158; 327/162	Chow; James et al.
19	340/10.1	O'Toole; James E. et al.
20		Davidson; Jeffrey et al.
21	340/10.1; 340/10.33 ; 342/42; 342/44; 342/51; 708/250; 708/252; 713/322	O'Toole; James E. et al.
22	340/10.33 ; 342/42; 342/44; 342/51	O'Toole; James E. et al.
23	340/10.1; 342/42; 342/44; 342/51	O'Toole; James E. et al.

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24	US 6803828 B2	20041012	8	Lock detector for phase locked loops	331/17
25	US 6774685 B2	20040810	53	Radio frequency data communications device	327/156
26	US 6771613 B1	20040803	53	Radio frequency data communications device	370/277
27	US 6763060 B1	20040713	11	Communication system employing a network of power managed transceivers that can generate a clocking signal or enable data bypass of a digital system associated with each transceiver	375/219
28	US 6735183 B2	20040511	55	Radio frequency data communications device	370/311
29	US 6721289 B1	20040413	55	Radio frequency data communications device	370/311
30	US 6696879 B1	20040224	53	Radio frequency data communications device	327/356
31	US 6600428 B1	20030729	53	Radio frequency data communications device	340/825.36
32	US 6580328 B2	20030617	8	Lock detector for phase locked loops	331/17
33	US 6504633 B1	20030107	15	Analog and digital electronic receivers for dual-use wireless data networks	398/183
34	US 6466634 B1	20021015	58	Radio frequency data communications device	375/374

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24	331/18; 331/23; 331/DIG. 2; 375/340; 375/376	Tan; Loke Kun et al.
25		O'Toole; James E. et al.
26		O'Toole; James E. et al.
27	327/147; 327/156; 375/222; 375/356; 375/376	Knapp; David J.
28	340/10.33 ; 342/42; 342/44; 342/51	O'Toole; James E. et al.
29	340/10.33 ; 342/42; 342/44; 342/51	O'Toole; James E. et al.
30	327/359	O'Toole; James E. et al.
31		O'Toole; James E. et al.
32	331/23; 331/25; 331/DIG. 2; 375/340; 375/376	Tan; Loke Kun et al.
33	398/130; 398/172	Hovorka; George B. et al.
34	340/10.1	O'Toole; James E. et al.

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35	US 6384648 B1	20020507	3345	Radio frequency data communications device	327/156
36	US 6362778 B1	20020326	16	Personal location detection system	342/357.07
37	US 6351190 B1	20020226	3343	Stage having controlled variable resistance load circuit for use in voltage controlled ring oscillator	331/57
38	US 6337634 B1	20020108	3336	Radio frequency data communications device	340/825.56
39	US 6316975 B1	20011113	3343	Radio frequency data communications device	327/156
40	US 6314440 B1	20011106	3339	Pseudo random number generator	708/250
41	US 6278698 B1	20010821	3337	Radio frequency data communications device	370/311
42	US 6211742 B1	20010403	9	Lock detector for phase locked loops	331/25
43	US 6198332 B1	20010306	3336	Frequency doubler and method of doubling frequency	327/356
44	US 6157230 A	20001205	3333	Method for realizing an improved radio frequency detector for use in a radio frequency identification device, frequency lock loop, timing oscillator, method of constructing a frequency lock loop and method of operating an integrated circuit	327/156
45	US 6130602 A	20001010	3335	Radio frequency data communications device	340/10.33
46	US 6005904 A	19991221	7	Phase-locked loop with protected output during instances when the phase-locked loop is unlocked	375/376
47	US 5952888 A	19990914	9	Roving range control to limit receive PLL frequency of operation	331/2

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35	331/57	O'Toole; James E. et al.
36	455/404.2	Neher; Timothy J
37	331/177R	O'Toole; James E. et al.
38	340/10.5	O'Toole; James E. et al.
39	327/159	O'Toole; James E. et al.
40	708/252; 713/322	O'Toole; James E. et al.
41	340/10.33	O'Toole; James E. et al.
42	331/DIG. 2	Tan; Loke Kun et al.
43	327/119; 327/122; 327/359	O'Toole; James E. et al.
44	327/159; 331/57	O'Toole; James E. et al.
45	342/42; 342/44; 342/51	O'Toole; James E. et al.
46	327/159; 331/DIG. 2	Knapp; David J. et al.
47	327/147; 331/11; 331/17; 331/23; 331/DIG. 2; 375/376	Scott; Paul H.

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48	US 5910960 A	19990608	70	Signal processing apparatus and method	714/784
49	US 5761210 A	19980602	68	Signal processing apparatus and method	714/701
50	US 5717715 A	19980210	72	Signal processing apparatus and method	375/220
51	US 5692020 A	19971125	69	Signal processing apparatus and method	375/350
52	US 5668831 A	19970916	78	Signal processing apparatus and method	375/232
53	US 5635864 A	19970603	70	Comparator circuit	327/77
54	US 5483558 A	19960109	10	Method and apparatus for detecting phase or frequency lock	375/376
55	US 5401026 A	19950328	28	Method and apparatus for determining parameters of the motion of an object	473/199
56	US 5246232 A	19930921	15	Method and apparatus for determining parameters of the motion of an object	473/199
57	US 4953185 A	19900828	13	Clock recovery and hold circuit for digital TDM mobile radio	375/354
58	US 4901026 A	19900213	6	Phase detector circuit having latched output characteristic	327/10
59	US 4801896 A	19890131	6	Circuit providing improved lock-in for a phase-locked loop	331/25
60	US 4744094 A	19880510	6	BPSK demodulator with D type flip/flop	375/327
61	US 4281412 A	19810728	20	Method of and apparatus for transmitting and recovering offset QPSK modulated data	375/332

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48		Claydon; Anthony Peter J. et al.
49		Claydon; Anthony Peter J. et al.
50	341/61	Claydon; Anthony Peter J. et al.
51	708/313	Robbins; William P.
52		Claydon; Anthony Peter J. et al.
53	327/63	Jones; Anthony M.
54	331/1A; 331/DIG. 2; 375/373	Leon; Ana S. et al.
55	342/109; 342/118; 473/155; 473/156; 473/225	Eccher; Daniel J. et al.
56	342/109; 342/118	Eccher; Daniel J. et al.
57	375/377	Goode; Steven H.
58	331/25	Phillips; Donald E. et al.
59	331/1R	Phillips; Donald E. et al.
60	329/307; 331/1A; 331/25; 331/DIG. 2	McCarren; Daniel S.
61	329/310; 375/376	Wissel; Frank A. et al.

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62	US 4078212 A	19780307	18	Dual mode frequency synthesizer for a television tuning apparatus	348/731

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62	331/DIG. 2; 455/180.2 ; 455/182.1 ; 455/183.1	Rast; Robert Morgan